

PATENT ABSTRACTS OF JAPAN

(11) Publication number : 02-000913
 (43) Date of publication of application : 05.01.1990

(51) Int. Cl. G02F 1/133
 G09G 3/20
 G09G 3/36

(21) Application number : 01-054028 (71) Applicant : MATSUSHITA ELECTRIC IND CO LTD
 (22) Date of filing : 07.03.1989 (72) Inventor : NAGATA SEIICHI
 KAWAMURA TETSUYA
 TAKEDA YOSHIYA
 MINAMINO YUTAKA

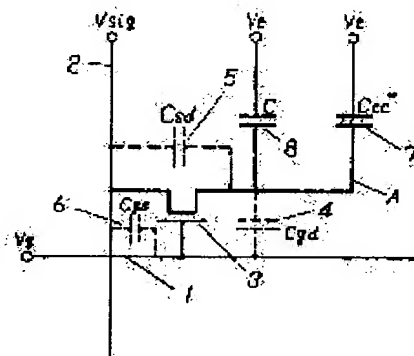
(30) Priority
 Priority number : 63 58765 Priority date : 11.03.1988 Priority country : JP

(54) DRIVING METHOD FOR DISPLAY DEVICE

(57) Abstract:

PURPOSE: To improve the display picture quality and driving reliability and to reduce the driving electric power by applying a 1st modulating signal to 1st wiring connected to picture element electrodes through storage capacitors and a 2nd modulating signal to counter electrodes, and modulating the potential difference between the 1st wiring and counter electrodes.

CONSTITUTION: Each display element has a TFT (thin film transistor) 3 at an intersection of scanning signal wiring 1 and image signal wiring 2, and the TFT has a parasitic capacity 4 and further a liquid crystal capacitor 7 and a storage capacitor 8 as capacitors which are formed intentionally. Then, the 2nd modulating signal V_t is applied to the counter electrode of the liquid crystal capacitor 7 and the 1st modulating signal V_e is applied to one electrode of the storage capacitor 8. Consequently, a capacitor coupling potential appearing at a picture element electrode (point A) through the various capacitors can be utilized effectively to compensate the dielectric anisotropy of liquid crystal and part of a DC component induced by a scanning signal through gate-drain capacity 4, the generation factors of a flicker, image memory property, etc., are removed to make a display of high quality, and the driving reliability of the display device is improved.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or

application converted registration]
[Date of final disposal for application]
[Patent number]
[Date of registration]
[Number of appeal against examiner's decision of
rejection]
[Date of requesting appeal against examiner's
decision of rejection]
[Date of extinction of right]

Copyright (C); 1998, 2000 Japan Patent Office